Getting Started in Assembly Programming with Keil uVision and MSP432

This tutorial is written on uVision v5.15 and Texas Instruments MSP432 LaunchPad.



Assembly Programming with MSP432

MSP432 has an ARM Cortex-M4F core. It supports Thumb-2 code only. As described in Chapter 8 of the text, with Unified Assembly Language, you may write assembly instructions the same way you write them for ARM processor. Even though the binary code generated for Thumb-2 is different than the ARM, they perform the same function. The only minor differences are in the ranges of the immediate value.

1. Launch Keil uVision.

Create a Project with Project Wizard

2. From the menu, select Project > New uVision Project...

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	Select Device for Target Remove Item	AH+ 57	

- 3. In the New uVision Project window, browse to the **Desktop**.
- 4. If you did not create a folder for the project before launching uVision, you may create a folder using the **New folder** menu item in the **Create New Project** window.

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5. Enter a name for the project folder. We will call it **project1** and click **Open**. This will bring us into the newly created folder **project1**.

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Save as <u>t</u> ype:	Project Files (*.uvproj; *.uvprojx)	Open Cancel	•

6. Enter a name for the project file. We will call it **proj1** and click **Save**.

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File name: F	proj1		11 ⁻¹	
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7. The Select Device for Target 'Target 1'... window will pop up. A list of devices supported will show up in the window at the lower left corner. These are the devices with the Device Family Software Pack installed. You may start drilling down the selections to find the device or type a substring of the device code in the Search window and the display in the window will narrow down to only the ones with match.

lect Device for Target 'Target 1'		
Vendor: <unknown></unknown>	-	
Device: <unknown> Toolset: <unknown> Search msp</unknown></unknown>		
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 P Texas Instruments MSP432 Family 		
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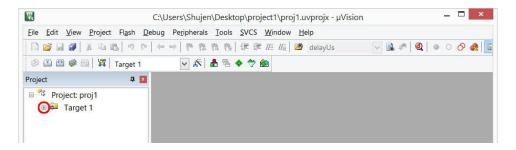
8. We will be using Texas Instruments MSP432P401R. To select this device, click on the + sign to expand the selections until you find the device. Click to highlight the device then click **OK** button.

PU)			
	Software Packs	-	
Vendor:	Texas Instruments		
Device:	MSP432P401R		
Toolset:	ARM		
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		Des <u>cription</u> .	
	Texas Instruments	The MSP432 family features the ARM Cortex-M4 processor in a wide configuration of device options including a inch set of analog, timing, and communication peripherals, thereby catering to a large number of application scenarios where both efficient data processing and enhanced low-power operation are paramount.	-
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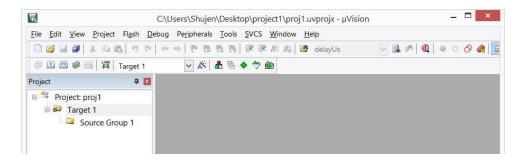
9. A dialog box to Manage Run-Time Environment pops up. Click OK to close it.

Software Component	Sel.	Variant	Version	Description
🗉 🚸 CMSIS				Cortex Microcontroller Software Interface Components
CMSIS Driver				Unified Device Drivers compliant to CMSIS-Driver Specifications
🗉 🗇 Compiler				ARM Compiler Software Extensions
🗉 🌵 Device				Startup, System Setup
🗉 🗇 File System		MDK-Pro	6.5.0	File Access on various storage devices
Graphics		MDK-Pro	5.30.0	User Interface on graphical LCD displays
Network		MDK-Pro	7.0.0	IPv4/IPv6 Networking using Ethernet or Serial protocols
🗄 🗇 USB		MDK-Pro	6.5.0	USB Communication with various device classes
۹				
Validation Output			Descript	ion

10. In the Project window, a target was created with the default name **Target 1**. Click on the + sign to the left of Target 1 to expand the folder.



11. A default folder for source code files was created with the name Source Group 1.



Add a Source File to the Project

12. Click the **New** button to add a new text file to the display with the default name **Text1**.

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13. From the menu, select **File > Save As...** to open the Save As dialog box. Browse to the project folder if it is not already there. Type in the file name **main.s** and click **Save**.

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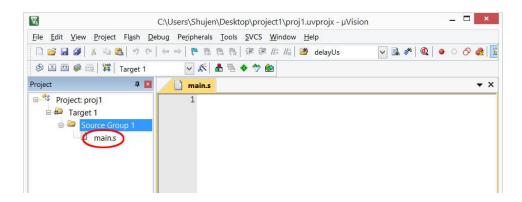
- 14. You will notice the file name in the tab changed to main.c
- 15. The new file needs be added to the project. Right click on the folder **Source Group 1** in the Project window and select **Add Existing Files to Group 'Source Group 1'...**

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Add New Item to Group 'Source Group 1' Add Existing Files to Group 'Source Group 1'	
Remove Group 'Source Group 1' and its Files	
Rebuild all target files Build target F7	
Manage Project Items	
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<	>
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Add Existing Files to current Project Group	ULINK2/t

16. In the dialog box, browse to the project folder if it is not already there. Change Files of type: to Asm Source file, Click select **main.s** then click **Add**.

Look in: 1 project1	▼ = 2 →
Name	Date modified
📜 Listings	10/23/2015 9:40
📙 Objects	10/23/2015 9:40
📔 main.s	10/23/2015 10:3
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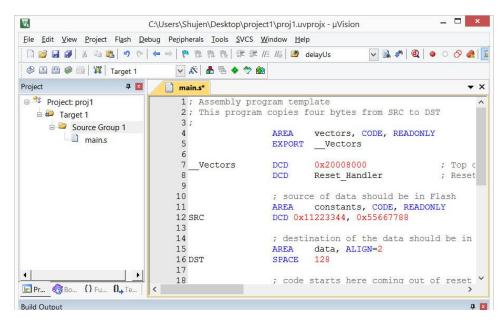
17. Click Close to close the dialog box. The file should appear in the project window under Source Group 1 folder.



18. Copy the following code into the main.s editor window.

```
main.s*
 1; Assembly program template
 2; This program copies four bytes from SRC to DST
 3;
 4
                          vectors, CODE, READONLY
                  AREA
 5
                  EXPORT
                          Vectors
 6
 7
                          0x20008000
    Vectors
                  DCD
                                                  ; Top of Stack
 8
                  DCD
                          Reset Handler
                                                  ; Reset Handler
 9
10
                  ; source of data should be in Flash
11
                        constants, CODE, READONLY
                  AREA
12 SRC
                  DCD 0x11223344, 0x55667788
13
14
                  ; destination of the data should be in RAM
15
                          data, ALIGN=2
                  AREA
16 DST
                  SPACE
                          128
17
18
                  ; code starts here coming out of reset
19
                  THUMB
20
                  AREA
                          program, CODE, READONLY
21
                  EXPORT Reset Handler
22 Reset Handler
23
                  LDR
                          R0, = main
24
                  BLX
                          RO
25
26; Your program starts here
27 main
28
                  LDR
                          R0, =SRC ; load R0 with source address
29
                  LDR
                          R1, [R0]
                                     ; load R1 through R0 indirect
30
                  LDR
                          R2, =DST ; load R2 with destination address
31
                  STR
                          R1, [R2]
                                     ; store R1 through R2
32
33
                  B
                                      ; stay here
                          12
34
                  ALIGN
35
                  END
36
```

19. The file name in the tab will have an '*' next to it. It symbolizes that the file has been changed without saving. You may click the save button to save the file or proceed to build the project. The file is automatically saved before the build.



Build the Project and Test with Simulator

20. Click on the **Build** button and wait for the build to complete. Make sure there are no error messages or warning messages.

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🖻 💭 Target 1	2; This program copies four bytes from SRC to DST
🖻 🗁 Source Group 1	3;
main.s	4 AREA vectors, CODE, READONLY
	5 EXPORT Vectors
	7 Vectors DCD 0x20008000 ; Top c
	8 DCD Reset Handler ; Reset
	9
	10 ; source of data should be in Flash
	11 AREA constants, CODE, READONLY
	12 SRC DCD 0x11223344, 0x55667788
	13 14 ; destination of the data should be in
	15 AREA data, ALIGN=2
	16 DST SPACE 128
•	17
	18 ; code starts here coming out of reset *
□ Pr	< >>
Build Output	4 🖬
assembling main.s	^
linking Program Size: Code=44 RO-data=0	DW_data=129_7T_data=0
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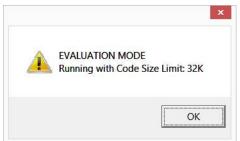
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- 21. Now we are ready to test the program with the simulator. Click on the **Options for Target** button.

22. Select **Debug** tab and click the radio button and select **Use Simulator** on the left to test the code with the simulator.

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Load Application at Startup Run to main() mitialization File: Edit		Image: Image						
🔽 Breakpoir	ndows & Performance Analyzer	Restore Debug Session Settings Image: Breakpoints Image: Toolbox Image: Watch Windows Image: Memory Display Image: System Viewer						
	Parameter:	Driver DLL: Parameter						
CPU DLL:		SARMCM3.DLL -MPU						
CPU DLL: SARMCM3.DLL	-MPU		Parameter:					
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23. Click OK button to close the Options window

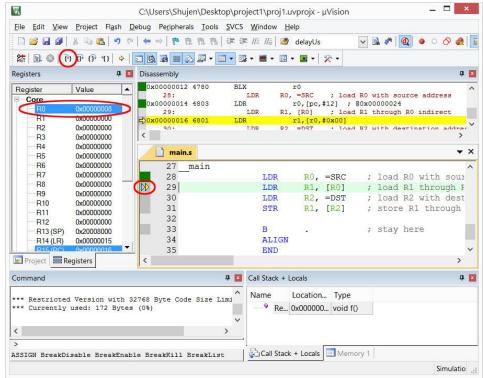
- 24. To simulate the program, you need to go into the debugger. Click the **Debug** button on the right.
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- 25. If you are running MDK-ARM Lite Edition, a message window will pop up to warn the code size limitation. Click **OK** to proceed.



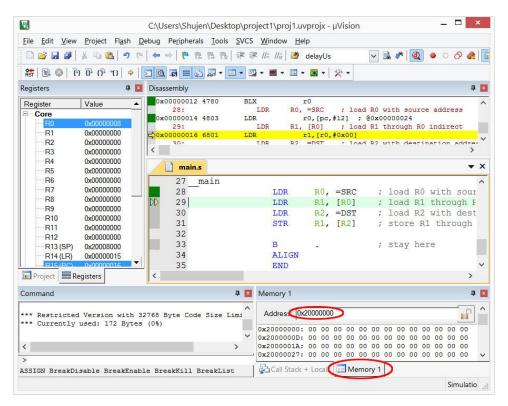
26. When entering the debugger, the IDE changes the perspective to the debug view. The program counter (which is indicated by a yellow triangle) is set at the beginning of the Reset_Handler.

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8 🗟 🚳	90+0+10 ⇒						
Registers	†	Disassembly	đ 🛔				
Register Value ▲ Core		OX000000E DCW Ox5566 23: LDR R0, = main c>0x00000010 4803 LDR r0,[pc,#12] ; @0x00000020 24: BLX R0 25: 26: Your program starts here					
	0x00000000 0x00000000						
R5	0x00000000	main.s	• ;				
	0x0000000 21 EXPORT Reset Handler						
	0x00000000 0x00000000	22 Reset_Handler					
	0x00000000	23 LDR R0, =_main 24 BLX R0					
R10 R11 R12 R13(SP)	0x00000000 0x00000000 0x00000000 0x20008000	25 26; Your program starts here 27 main					
- R14 (LR)		28 LDR R0, =SRC ; load R0 with	1 sour				
R15 (PC)	and the second second second second	LDR R1, [R0] ; load R1 thro	ough F				
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	d Version with a used: 172 Byte	32768 Byte Code Size Limi Name Location Type					
>		Dle BreakKill BreakList					
	isable BreakEnab						

27. Click Step button until the execution passes instruction "LDR R0, =SRC" (The pointer is pointing to the address of the PC, which is the next instruction to be executed). This instruction loads R0 with the address of SRC. When the content of the R0 changed, it is highlighted as seen below.



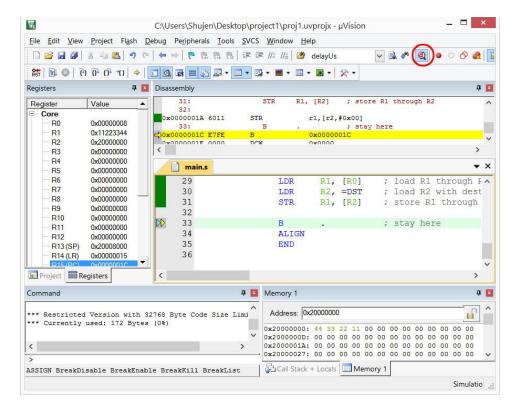
28. At the lower right window, click **Memory 1** tab and type 0x20000000 in the **Address** box. You should see that the memory content is all 0. We will see the memory content changes next.



29. Step until the execution passes the instruction "STR R1, [R2]" instruction. This instruction writes the content of R1 to memory with address in R2. Since R1 has 0x11223344 and R2 has 0x20000000, the value 0x11223344 is written to memory location 0x20000000. You should see the value in the memory window.

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R1	0x11223344	⇒0x0000001C E7FE	В	2 .	0x00000		nere					
R2	0x20000000	0x0000001E 0000	DCT		0x00000	020						
R3	0x00000000	<									>	
	0x00000000											
R5	0x00000000	main.s									•	1
R6	0x00000000	29		LDR	R1.	[R0]	1.5	load	R1	thro	ugh I	1
	0x00000000	30		LDR		=DST		load				
R8	0x00000000	31		STR								1
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R10	0x00000000	32										
	0x00000000	33		В	10-1		;	stay	her	е		
	0x00000000	34		ALIGN								
- R13 (SP)		35		END								j
R14 (LR)	0x00000015	36										
R15 (PC)	0×0000001C											1
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										S	imulatio	Э

30. Exit debugger by clicking on the debug button again.



This concludes the tutorial.